



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Inventors: Michael C. Dorsey § Atty. Dkt. No.: 5681-56300

Serial No.: 09/976,523 § Art Unit: 2858

Filing Date: October 12, 2001 § Examiner: Unknown

For: Utilizing Slow ASIC Logic BIST to
Preserve Timing Integrity
Across Timing Domains

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NOTICE OF CHANGE OF ADDRESS

Commissioner for Patents
Washington, D.C. 20231

Sir:

Applicant respectfully requests the Commissioner to change the correspondence address
for the above identified patent application. The old correspondence address was:

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If there are any questions regarding this matter, please contact me at the telephone number
provided below.

Respectfully submitted,

B. Noël Kivlin
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